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	Filing Date		2006-07-25	
	First Named Inventor	Andreas Bode		
	Art Unit	1621		
	Examiner Name			
Attorney Docket Number		2003P18668WOUS		

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	1	5159185	JP	A1	1993-06-25	Toshiba Corp		<input type="checkbox"/>

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1	FUJISHIMA et al., "Band Runlength coding -68 for low-power continuous micro-monitors", Custom Integrated Circuits, 1999, proceedings of the IEEE 1999 San Diego, CA, USA, May 16-19, 1999, Pages 291 – 294, XP 010340797	<input type="checkbox"/>
2	SAYTINER, et al., "A Level-Crossing Sampling Scheme for A/D Conversion", IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing, New York, Vol. 43, April 1, 1996, Pages 335 – 339, page 335, left column, line 12 – line 13, XP 000592026	<input type="checkbox"/>

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